

# The leader in high temperature semiconductors for demanding markets

# Job Opening: Analog IC Layout Engineer

CISSOID is the leader in high temperature semiconductors for demanding markets. With a focus on the Automotive Market we deliver solutions for efficient power conversion and compact motor drives: high voltage gate drivers for SiC & GaN transistors, Power Modules featuring low inductances and enhanced thermal performance, and automotive grade components rated at 175°C in excess of the AEC-Q100 Grade 0 qualification standard.

Join the crew of Cissoid, a company with strong technology leadership in its market, and participate in the development of IC's in an enthusiastic team. For the development of new high-end Gate Driver Integrated Circuits for Silicon Carbide (SiC) and Gallium Nitride (GaN) power transistors, CISSOID is hiring a **Analog IC Layout Engineers**:

#### **Duties**

- Performs layout and carries corrective actions to meet the requirement of the layout specification including the remarks from review made with IC Design Engineers.
- Performs DRC and corrective actions until the Layout is DRC error free
- Performs LVS and corrective actions until result is successful
- Provides extracted netlist to IC Design Engineers for post layout simulations
- Translates sub block schematics to sub block layouts, taking care of the same hierarchical build-up
- Prepare layout reports and documentation
- Reports proactively progresses to project leader.

## **Experience**

- Analog IC Layout Engineer with 5+ years demonstrated experience in the successful design of Analog ICs in CMOS with exposure to Analog Mixed Signal in a semiconductor company.
- Must possess expertise in Analog IC Layout with a comprehensive understanding of high voltage and high current ICs
- Experience with High voltage BCD SOI processes is a plus
- Hands-on experience in the layout of gate drivers, DC-DC converters, linear regulators, voltage references, amplifiers and comparators is a plus though not mandatory
- Knowledge and ability to use the analog design tools environment (such as Tanner L-Edit and/or Cadence, Mentor Calibre ...)

### **Skills**

- Excellent problem solving and analytical skills.
- Adhering to schedule, cost and quality objectives is of real importance to you.
- Ability to work in a multithreading, dynamic and challenging environment.
- You are concerned about innovation and efficiency leading to improving your operations.
- You are a self motivated professional able to work alone of as part of a multicultural team
- You are able to translate general instructions into detailed implementation and take your responsibilities.
- Fluent in English, both written and spoken

### Offer

- CISSOID offers a challenging job with an attractive salary including bonus based on company objectives and compensation benefits.
- CISSOID offers a multicultural work environment. Candidates with multiple competencies liking multi-tasking will find working at CISSOID a very enriching experience.
- The job is **open to freelance engineers** with the possibility to partly work remotely.

#### Location:

This position will be located in Mont-Saint-Guibert, Belgium (30 km south of Brussels).

#### **Contact**

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