

# CHT-LDOP Datasheet

Version: 2.8  
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(Last Modification Date)

## High-Temperature, 1A, Low-Dropout Voltage Regulator 5V; (2.5V; 3.3V, 5.5V; 5.6V; 7.5V, 9V; 10V; 12V; 13V or 15V)

### General Description

The CHT-LDOP is a 1A, low-dropout linear voltage regulator compatible with high-temperature environments. Typical operation temperature range extends from -55°C to 225°C.

The circuit is stable throughout the whole temperature range and under a large choice of capacitive loads.

The minimum dropout voltage ( $V_{in}-V_{out}$ ) is 2V with a 1A load current at 225°C and 1V for load currents lower than 400mA. The dropout voltage can span from 1 Volts to 20 Volts<sup>(1)</sup>.

The circuit is a one-die solution.

Related documents:

- **AN-06016:** "Selecting correct CISSOID regulator depending on your application"
- **AN-06002:** "Voltage regulator short-circuit protection and associated potential startup problem".
- **AN-090477:** "Power Dissipation Considerations During Short Circuit Conditions"

**CISSOID recommends the use of CHT-LDOS device since it is suitable for both unipolar and bipolar power supplies and has the same characteristics as CHT-LDOP except the short-circuit current**

### Applications

Power supplies for high-temperature electronic systems used in Well logging, Automotive, Aeronautics or Aerospace applications.

### Features

- 1V to 20V dropout Voltage @400mA<sup>(1)</sup>
- 2V to 20V dropout Voltage @1A<sup>(1)</sup>
- Max 1A output current @ 225°C
- 60dB input ripple rejection (0-100Hz)
- $C_{load}$  from 100nF to 1000 $\mu$ F, large ESR range
- Output voltage programmable by bonding option (bare die version)
- Available in TO-254 package and bare die form (contact CISSOID)
- The start-up is operative over the whole temperature range
- Latch-up free
- Validated at 225°C for 43800 hours (and still on-going)

### Active

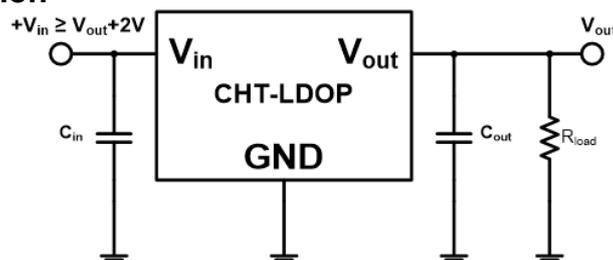
- CHT-LDOP-050: 5V

### Legacy (Not for new designs)

- CHT-LDOP-033: 3.3V
- CHT-LDOP-120: 12.0V
- CHT-LDOP-150: 15.0V
- CHT-LDOP-025: 2.5V
- CHT-LDOP-055: 5.5V
- CHT-LDOP-056: 5.6V
- CHT-LDOP-075: 7.5V
- CHT-LDOP-090: 9.0V
- CHT-LDOP-100: 10.0V
- CHT-LDOP-130: 13.0V

Note that these legacy devices can be replaced by CHT-LDOS equivalent: see section "Replacement of CHT-LDOP by CHT-LDOS" on page 4.

### Typical application



### Absolute Maximum Ratings

Supply Voltage $V_{in}$ to GND	-0.3V to 40V
Junction Temperature <sup>(2)</sup> $T_j$	300°C
Power dissipation <sup>(3)</sup>	

### Operating Conditions

Supply Voltage $V_{in}$ to GND	$V_{out}+(1V \text{ to } 20V)^{(1)}$
Junction temperature	-55°C to +225°C
Power dissipation <sup>(3)</sup>	

### ESD Rating (expected)

Human Body Model	<1kV
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**CAUTION:** Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Frequent or extended exposure to absolute maximum rating conditions or above may affect device reliability.

## Electrical Characteristics

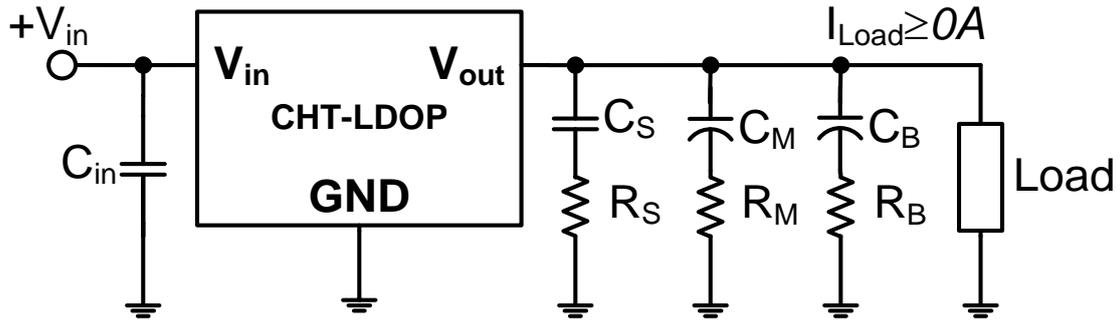
Unless otherwise stated:  $V_{in}=17V$ ,  $V_{out}=15V$  (CHT-LDOP-150). **Bold underlined** values indicate values over the whole temperature range ( $-55^{\circ}C < T_j < +225^{\circ}C$ ). For other nominal voltages, see notes under this table.

Parameter	Condition	Min	Typ	Max	Units
Input voltage $V_{in}^{(1)}$	$-55^{\circ}C < T_j < 225^{\circ}C$			<b>30</b>	V
Dropout voltage ( $V_{in}-V_{out}$ )	$I_L < 400mA$ $-55^{\circ}C < T_j < 225^{\circ}C$	<b>1</b>			V
	$I_L = 1A$ $-55^{\circ}C < T_j < 225^{\circ}C$	<b>2</b>			
Output voltage accuracy	$I_L = 10mA$ $-55^{\circ}C < T_j < 225^{\circ}C$	<b>-2</b>		<b>+2</b>	%
Output voltage temperature drift <sup>(4)</sup>	$I_L = 10mA$ $+25^{\circ}C < T_j < 225^{\circ}C$	0	40	80	ppm
Output voltage line regulation (PSRR <sub>DC</sub> ) <sup>(5)</sup>	$(V_{in}-V_{out}) = 2V$ to 15V $I_L = 10mA$ $-55^{\circ}C < T_j < 225^{\circ}C$	<b>-3</b>		<b>+3</b>	mV/V
Output voltage load regulation (R <sub>out</sub> ) <sup>(6)</sup>	$I_L = 10mA$ to 1A $(V_{in}-V_{out}) = 2V$ $-55^{\circ}C < T_j < 225^{\circ}C$		<b>40</b>	<b>100</b>	mV/A
Quiescent ground pin current <sup>(7)</sup>	$I_L < 1A$ $T_j = -55^{\circ}C$		4.1		mA
	$I_L < 1A$ $T_j = +225^{\circ}C$		3.8		
Foldback current	$(V_{in}-V_{out}) = 2V$ to 15V $-55^{\circ}C < T_j < 225^{\circ}C$	<b>1.5</b>	<b>2</b>	<b>2.5</b>	A
Short circuit current $I_{sc}$	$(V_{in}-V_{out}) = 2V$ to 15V $-55^{\circ}C < T_j < 225^{\circ}C$		80		mA
Power supply rejection ratio PSRR <sup>(8)</sup>	$f = 0$ to 100Hz $I_L = 100mA$ $-55^{\circ}C < T_j < 225^{\circ}C$		<b>60</b>		dB
Output noise $e_{on}$	10Hz to 10kHz $I_L = 100mA$ $-55^{\circ}C < T_j < 225^{\circ}C$		<b>tbd</b>		V <sub>rms</sub>
Junction-to-ambient thermal resistance (free air) $R_{thja}$	TO-254 package		50		$^{\circ}C/W$
Junction-to-case thermal resistance $R_{thjc}$	TO-254 packages		5		$^{\circ}C/W$

### Notes:

- (1) The maximum input voltage  $V_{in}$  is  $V_{out}+20V$  or 30V, whichever is less.
- (2) Above 225°C ( $T_j$ ), a minimum load current of few mA could be required.
- (3) Max Power dissipation depends on packaging.
- (4) ppm are defined as  $1e-6 \times [d(V_{out})/d(T)]/V_{out}$ . For a nominal output of 15V, 40ppm corresponds to 600 $\mu$ V/ $^{\circ}C$ .
- (5) Defining "x" as the nominal voltage, the line regulation is better than  $x/5$  mV/V.
- (6) This includes the packaging parasitic resistance for TO-254 packages.
- (7) Defining "x" as the nominal voltage, the typical quiescent current at 2V dropout can be approximated as  $2.95+x/13$  in mA @  $-55^{\circ}C$  and  $2.65+x/13$  in mA at 225°C.
- (8) Preliminary data.

Input Capacitor and Output Load Recommended Specifications



Resistances in series with capacitors represent the internal ESR of these capacitors.

**Output capacitor recommendations:**

Equivalent  $C_{out} \geq 220nF$  with low ESR

Large capacitors:

$$C_B = 0-1000\mu F$$

$$R_B = 0.2-\infty\Omega$$

Medium capacitors:

$$C_M = 0-6\mu F$$

$$R_M = 0.1-1\Omega$$

Small capacitors:

$$C_S = 220-1000nF$$

$$R_S = 10-50m\Omega$$

**Input capacitor recommendations:**

Equivalent  $C_{in} \geq 220nF$  with low ESR

## Operating Conditions

### Start-up conditions

The start-up is operative over the whole temperature range as long as all loads are connected to ground. The start-up is not guaranteed if the positive regulator output has a current path directly connected to a negative voltage. Indeed, such load condition can lead to wrong activation of the short-circuit protection, i.e. a bad start-up or a bad recovering after short-circuit. In this case, it is recommended to use our CHT-LDOS regulator family instead of CHT-LDOP regulator family.

Please refer to our application notes for more details:

- **AN-06016:** “Selecting correct CISSOID regulator depending on your application”
- **AN-06002:** “Voltage regulator short-circuit protection and associated potential startup problem”.

### Power dissipation considerations

When determining the maximum power dissipated by the regulator, not only the dissipation during normal operation must be considered, but also the power dissipated during any eventual short circuit or overload.

During short circuit or overload, worst case conditions are normally found for maximum  $V_{in}$  and a shorting resistance in the order of few Ohms.

Entering into short-circuit or overload conditions with high input voltages  $V_{in}$  may lead to extreme overheating, placing the part above Absolute Maximum Rating conditions.

Please refer to our application note for more detail:

- **AN-090477:** “Power Dissipation Considerations During Short Circuit Conditions”

### Shorting the regulator input

If the input terminal is shorted to ground once the output capacitance has been charged, a large current corresponding to the discharge of the output capacitor will flow from the output to the input through the drain-body diode of the internal pass transistor. This large current may cause the permanent damage of the part.

**Sinking current or raising the output voltage above the input voltage can cause permanent damage to the part.**

### Regulator floating ground

When the ground becomes disconnected, the output voltage gets unregulated, causing possible damage to other circuits connected to  $V_{out}$ . If the ground terminal is reconnected while  $V_{in}$  is applied, permanent damage may also occur to the regulator. If a regulator needs to be reconnected with the power supply on, then connect the ground terminal first.

## Replacement of CHT-LDOP by CHT-LDOS equivalent

Except for CHT-LDOP-050, all other CHT-LDOP have now the status “legacy” or “not for new designs”. They can be replaced by their CHT-LDOS equivalent:

Legacy CHT-LDOP part	Replacement
CHT-LDOP-025-TO254-T	CHT-LDOS-025-TO254-T
CHT-LDOP-033-TO254-T	CHT-LDOS-033-TO254-T
CHT-LDOP-055-TO254-T	CHT-LDOS-055-TO254-T
CHT-LDOP-056-TO254-T	CHT-LDOS-056-TO254-T
CHT-LDOP-075-TO254-T	CHT-LDOS-075-TO254-T
CHT-LDOP-090-TO254-T	CHT-LDOS-090-TO254-T
CHT-LDOP-100-TO254-T	CHT-LDOS-100-TO254-T
CHT-LDOP-120-TO254-T	CHT-LDOS-120-TO254-T
CHT-LDOP-130-TO254-T	CHT-LDOS-130-TO254-T
CHT-LDOP-150-TO254-T	CHT-LDOS-150-TO254-T

CHT-LDOS equivalent products are pin-to-pin compatible to CHT-LDOP. The products use the same die and have the same electrical characteristics except for the short circuit current (300mA for CHT-LDOS with respect to 80mA for CHT-LDOP). No difference in normal operation is expected between two equivalent products.

The datasheet of CHT-LDOS devices can be found in:

<https://www.cissoid.com/files/files/products/star/cht-ldos.pdf>

**Typical Performance Characteristics (CHT-LDOP-150)**  
 Note: Temperatures hereafter are ambient temperatures.

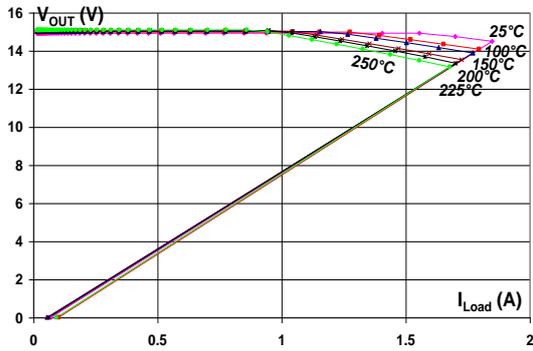


Figure 1:  $V_{out}$  vs.  $I_{Load}$  @ 2V dropout

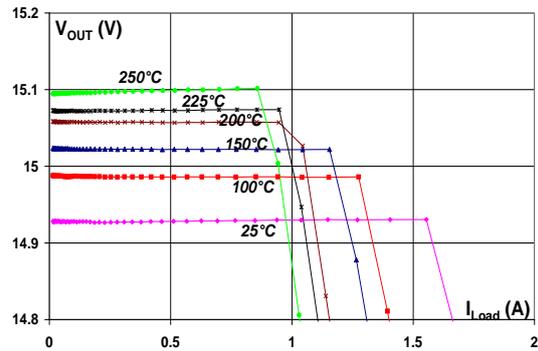


Figure 2: Zoom on figure 1

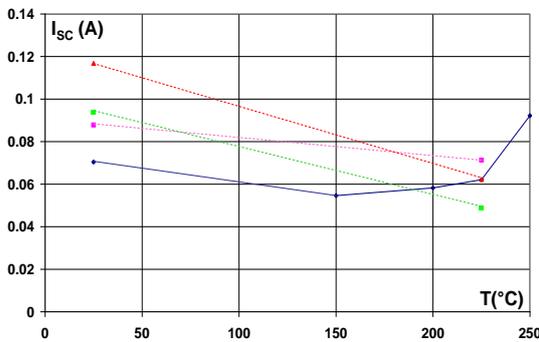


Figure 3: Typical short-circuit current vs.  $T^\circ$  (4 samples, 2V dropout)

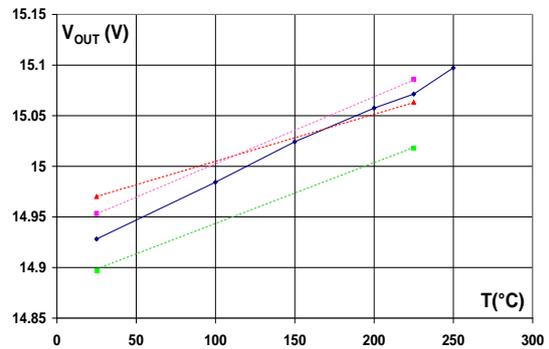


Figure 4:  $V_{out}$  vs.  $T^\circ$  (2V dropout, 4 samples)

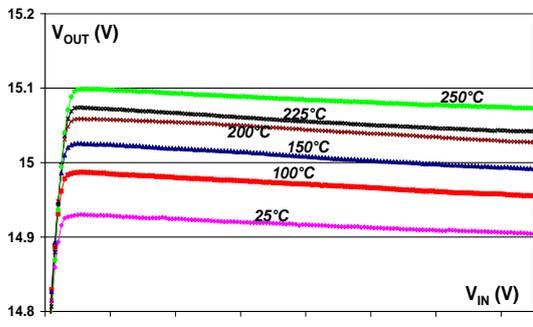


Figure 5:  $V_{out}$  vs.  $V_{in}$  over  $T^\circ$

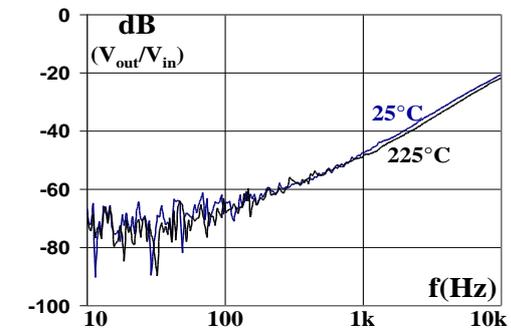


Figure 6: Input ripple rejection ( $C_{out} = 1\mu F$ )

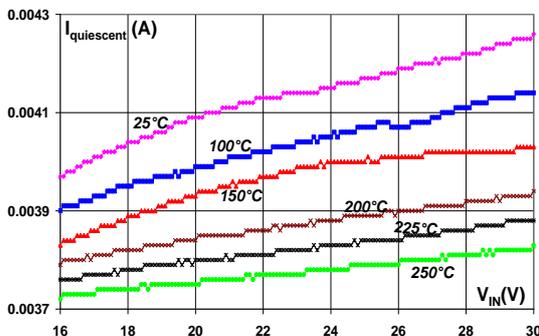


Figure 7:  $I_{quiescent}$  vs.  $V_{in}$  over  $T^\circ$

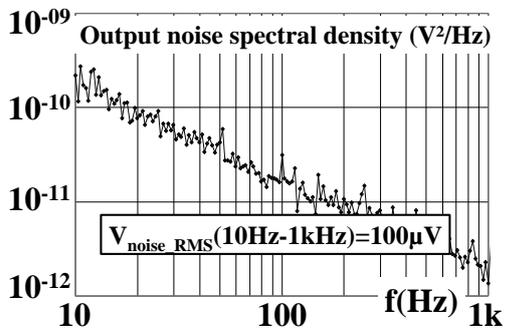


Figure 8:  $S_{V_{out}}(V^2/Hz)$  @  $25^\circ C$ ,  $I_{Load}=100mA$

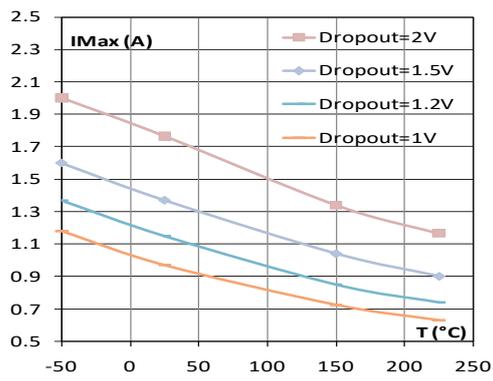
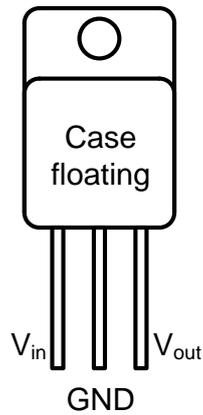


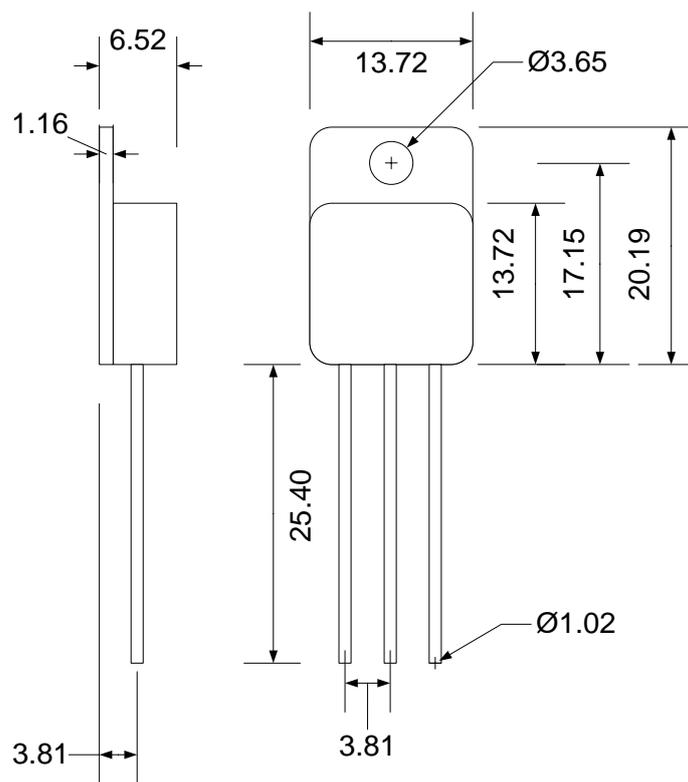
Figure 9: Typical max load current over T° vs dropout voltage

## Packaging and Pinout



**TO-254 (Front view)**

## Package Dimensions



TO254 (mm +/- 10%)

## Ordering Information

Active

Ordering Reference	Package	Output Voltage	Temperature Range	Marking
CHT-LDOP-050-TO254-T	Metal TO254	5V	-55°C to +225°C	CHT-LDOP-050

Legacy (Not for new designs)<sup>1</sup>

Ordering Reference	Package	Output Voltage	Temperature Range	Marking
CHT-LDOP-025-TO254-T	Metal TO254	2.5V	-55°C to +225°C	CHT-LDOP-025
CHT-LDOP-033-TO254-T	Metal TO254	3.3V	-55°C to +225°C	CHT-LDOP-033
CHT-LDOP-055-TO254-T	Metal TO254	5.5V	-55°C to +225°C	CHT-LDOP-055
CHT-LDOP-056-TO254-T	Metal TO254	5.6V	-55°C to +225°C	CHT-LDOP-056
CHT-LDOP-075-TO254-T	Metal TO254	7.5V	-55°C to +225°C	CHT-LDOP-075
CHT-LDOP-090-TO254-T	Metal TO254	9V	-55°C to +225°C	CHT-LDOP-090
CHT-LDOP-100-TO254-T	Metal TO254	10V	-55°C to +225°C	CHT-LDOP-100
CHT-LDOP-120-TO254-T	Metal TO254	12V	-55°C to +225°C	CHT-LDOP-120
CHT-LDOP-130-TO254-T	Metal TO254	13V	-55°C to +225°C	CHT-LDOP-130
CHT-LDOP-150-TO254-T	Metal TO254	15V	-55°C to +225°C	CHT-LDOP-150

<sup>1</sup> Note that these legacy devices can be replaced by CHT-LDOS equivalent: see section "Replacement of CHT-LDOP by CHT-LDOS" on page 4

## Contact & Ordering

CISSOID S.A.

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<b>Sales Representatives:</b>	<b>Visit our website: <a href="http://www.cissoid.com">http://www.cissoid.com</a></b>

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